19-1958; Rev 2; 1/02

MAXIM

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

General Description

The LMX331/LMX393/LMX339 single/dual/quad comparators are drop-in, pin-for-pin-compatible replacements for the LMV331/LMV393/LMV339. The LMX331H/LMX393H/LMX339H offer the performance of the LMX331/LMX393/LMX339 with the added benefit of internal hysteresis to provide noise immunity, preventing output oscillations even with slow-moving input signals.

Advantages of the LMX331/LMX393/LMX339 series include low supply voltage, small package, and low cost. The LMX331 is available in both 5-pin SC70 and SOT23 packages, LMX393 is available in both 8-pin µMAX and smaller SOT23 packages, and the LMX339 is available in 14-pin TSSOP and SO packages. They are manufactured using advanced submicron CMOS technology. Designed with the most modern techniques, the LMX331/LMX393/LMX339 achieve superior performance over BiCMOS or bipolar versions on the market.

The LMX331/LMX393/LMX339 offer performance advantages such as wider supply voltage range, wider operating temperature range, better CMRR and PSRR, improved response time characteristics, reduced offset, reduced output saturation voltage, reduced input bias current, and improved RF immunity.

Applications

Mobile Communications

Notebooks and PDAs

Automotive Applications

Battery-Powered Electronics

General-Purpose Portable Devices

General-Purpose Low-Voltage Applications

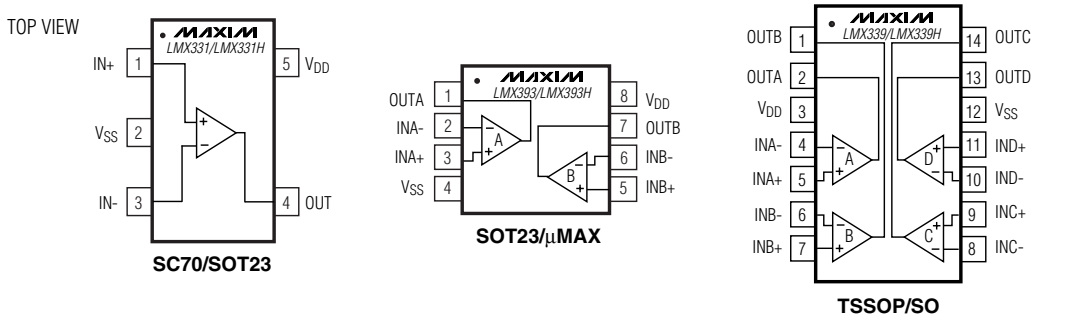
Features

- **Guaranteed 1.8V to 5.5V Performance**
- **-40°C to +125°C Automotive Temperature Range**
- **Low Supply Current (60µA/Comparator at V_{DD} = 5.0V)**
- **Input Common-Mode Voltage Range Includes Ground**
- **No Phase Reversal for Overdriven Inputs**
- **Low Output Saturation Voltage (100mV)**
- **Internal 2mV Hysteresis (LMX331H/LMX393H/LMX339H)**
- **5-Pin SC70 Space-Saving Package (2.0mm × 2.1mm × 1.0mm) (LMX331/LMX331H)**

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
LMX331AXK-T	-40°C to +125°C	5 SC70-5	ACD
LMX331AUK-T	-40°C to +125°C	5 SOT23-5	ADQR
LMX331HAXK-T	-40°C to +125°C	5 SC70-5	ACE
LMX331HAUK-T	-40°C to +125°C	5 SOT23-5	ADQS
LMX393AKA-T	-40°C to +125°C	8 SOT23-8	AAIF
LMX393AUA	-40°C to +125°C	8 µMAX	—
LMX393HAKA-T	-40°C to +125°C	8 SOT23-8	AAIG
LMX393HAUA	-40°C to +125°C	8 µMAX	—
LMX339AUD	-40°C to +125°C	14 TSSOP	—
LMX339ASD	-40°C to +125°C	14 SO	—
LMX339HAUD	-40°C to +125°C	14 TSSOP	—
LMX339HASD	-40°C to +125°C	14 SO	—

Pin Configurations



SC70/SOT23

SOT23/µMAX

TSSOP/SO

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_{DD} to V_{SS}).....-0.3V to +6V
All Other Pins ..... (V_{SS} - 0.3V) to (V_{DD} + 0.3V)
Continuous Power Dissipation (T_A = +70°C)
5-Pin SC70 (derate 3.1mW/°C above +70°C).....247mW
5-Pin SOT23 (derate 7.1mW/°C above +70°C).....571mW
8-Pin SOT23 (derate 8.9mW/°C above +70°C).....714mW
8-Pin µMAX (derate 10.3mW/°C above +70°C).....825mW
14-Pin TSSOP (derate 9.1mW/°C above +70°C).....727mW
14-Pin SO (derate 8.3mW/°C above +70°C).....666.7mW
Operating Temperature Range .....-40°C to +125°C
Junction Temperature.....+150°C
Storage Temperature Range .....-65°C to +150°C
Lead Temperature (soldering, 10s).....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS—2.7V OPERATION

(V_{DD} = 2.7V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ connected to V_{DD}. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	V_{OS}			0.2	7	mV
Input Voltage Hysteresis	V_{HYST}	LMX331H/LMX393H/LMX339H only		2		mV
Input Offset Voltage Average Temperature Drift	TCV_{OS}			5		µV/°C
Input Bias Current	I_B	T_A = +25°C		±0.05	±250	nA
		T_A = -40°C to +85°C			±400	
		T_A = -40°C to +125°C			±400	
Input Offset Current	I_{OS}	T_A = +25°C		±0.05	±50	nA
		T_A = -40°C to +85°C			±150	
		T_A = -40°C to +125°C			±150	
Input Voltage Range	V_{CM}			-0.1		V
				2.0		
Voltage Gain	A_V	LMX331/LMX393/LMX339 only		50		V/mV
Output Saturation Voltage	V_{SAT}	I_{SINK} ≤ 1mA		50		mV
Output Sink Current	I_O	V_O ≤ 1.5V	5	37		mA
Supply Current (Note 2)	I_S	LMX331/LMX331H		50	100	µA
		LMX393/LMX393H (both comparators)		70	140	
		LMX339/LMX339H (all four comparators)		140	200	
Output Leakage Current		T_A = +25°C		0.003		µA
		T_A = -40°C to +85°C			1	
		T_A = -40°C to +125°C			2	

AC ELECTRICAL CHARACTERISTICS—2.7V OPERATION

(V_{DD} = 2.7V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ connected to V_{DD}. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Propagation Delay Output High to Low	t_{PHL}	Input overdrive = 10mV (Note 3)		500		ns
		Input overdrive = 100mV (Note 3)		100		
Propagation Delay Output Low to High	t_{PLH}	Input overdrive = 10mV (Note 3)		500		ns
		Input overdrive = 100mV (Note 3)		100		

DC ELECTRICAL CHARACTERISTICS—5.0V OPERATION

(V_{DD} = 5V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ connected to V_{DD}. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Input Offset Voltage	V_{OS}	T_A = +25°C			0.25	7	mV
		T_A = -40°C to +85°C				9	
		T_A = -40°C to +125°C				9	
Input Voltage Hysteresis		LMX331H/LMX393H/LMX339H only			2		mV
Input Offset Voltage Average Temperature Drift	TCV_{OS}				5		µV/°C
Input Bias Current	I_B	T_A = +25°C			±0.05	±250	nA
		T_A = -40°C to +85°C				±400	
		T_A = -40°C to +125°C				±400	
Input Offset Current	I_{OS}	T_A = +25°C			±0.05	±50	nA
		T_A = -40°C to +85°C				±150	
		T_A = -40°C to +125°C				±150	
Input Voltage Range	V_{CM}				-0.1		V
					4.2		
Voltage Gain	A_V	LMX331/LMX393/LMX339 only		20	50		V/mV
Output Saturation Voltage	V_{SAT}	I_{SINK} ≤ 4mA	T_A = +25°C		70	400	mV
			T_A = -40°C to +85°C			700	
			T_A = -40°C to +125°C			700	
Output Sink Current	I_O	V_O ≤ 1.5V		10	73		mA
Supply Current (Note 2)	I_S	LMX331/LMX331H	T_A = +25°C		60	120	µA
			T_A = -40°C to +85°C			150	
			T_A = -40°C to +125°C			170	
		LMX393/LMX393H (both comparators)	T_A = +25°C		100	200	
			T_A = -40°C to +85°C			250	
			T_A = -40°C to +125°C			300	
		LMX339/LMX339H (all four comparators)	T_A = +25°C		170	300	
			T_A = -40°C to +85°C			350	
			T_A = -40°C to +125°C			430	
Output Leakage Current		T_A = +25°C			0.003		µA
		T_A = -40°C to +85°C				1	
		T_A = -40°C to +125°C				2	

AC ELECTRICAL CHARACTERISTICS—5.0V OPERATION

(V_{DD} = 5V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ connected to V_{DD}. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Propagation Delay Output High to Low	t_{PHL}	Input overdrive = 10mV (Note 3)		400		ns
		Input overdrive = 100mV (Note 3)		90		
Propagation Delay Output Low to High	t_{PLH}	Input overdrive = 10mV (Note 3)		600		ns
		Input overdrive = 100mV (Note 3)		200		

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

DC ELECTRICAL CHARACTERISTICS—1.8V OPERATION

(V_{DD} = 1.8V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ connected to V_{DD}. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	V_{OS}			0.2	5	mV
Input Voltage Hysteresis		LMX331H/LMX393H/LMX339H only		2		mV
Input Offset Voltage Average Temperature Drift	TCV_{OS}			5		µV/°C
Input Bias Current	I_B			0.05		nA
Input Offset Current	I_{OS}			0.05		nA
Input Voltage Range	V_{CM}			-0.1		V
				1		
Output Saturation Voltage	V_{SAT}	I_{SINK} ≤ 1mA		35		mV
Power-Supply Rejection Ratio	PSRR	V_{DD} = 1.8V to 5.5V	60	70		dB
Output Sink Current	I_O	V_O ≤ 1.5V		15		mA
Supply Current (Note 2)	I_S	LMX331/LMX331H		40	100	µA
		LMX393/LMX393H (both comparators)		65	140	
		LMX339/LMX339H (all four comparators)		120	200	
Output Leakage Current				0.003		µA

AC ELECTRICAL CHARACTERISTICS—1.8V OPERATION

(V_{DD} = 1.8V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ connected to V_{DD}. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Propagation Delay Output High to Low	t_{PHL}	Input overdrive = 10mV (Note 3)		500		ns
		Input overdrive = 100mV (Note 3)		100		
Propagation Delay Output Low to High	t_{PLH}	Input overdrive = 10mV (Note 3)		500		ns
		Input overdrive = 100mV (Note 3)		100		

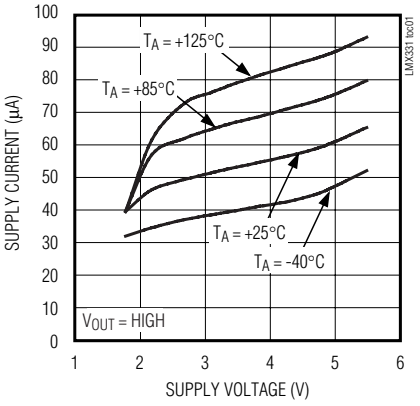
Note 1: All devices are production tested at +25°C. All temperature limits are guaranteed by design.
Note 2: Supply current when output is high.
Note 3: Input overdrive is the overdrive voltage beyond the offset and hysteresis-determined trip points.

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

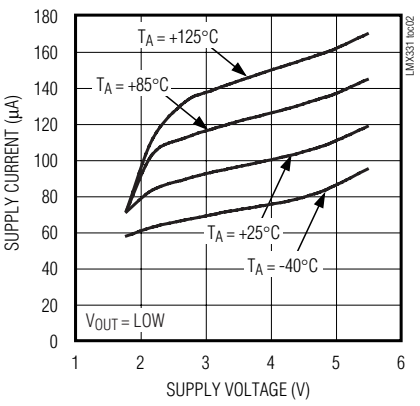
Typical Operating Characteristics

(V_{DD} = 5V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ, C_L = 10pF, overdrive = 100mV, T_A = +25°C, unless otherwise noted.)

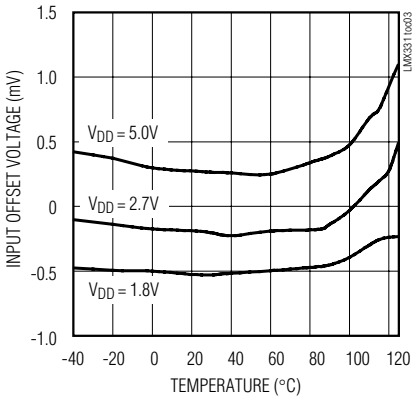
LMX331
SUPPLY CURRENT vs. SUPPLY VOLTAGE



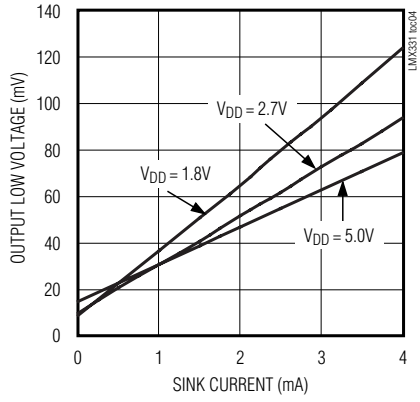
LMX331
SUPPLY CURRENT vs. SUPPLY VOLTAGE



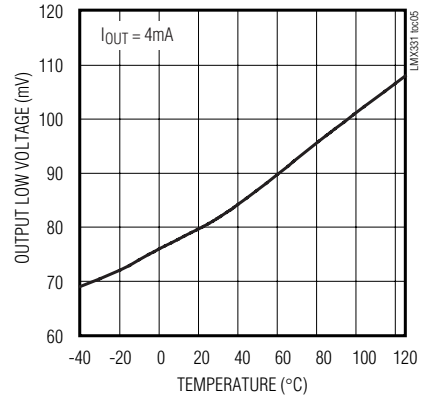
INPUT OFFSET VOLTAGE vs. TEMPERATURE



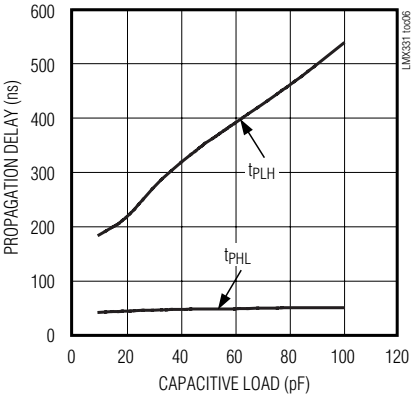
OUTPUT LOW VOLTAGE vs. SINK CURRENT



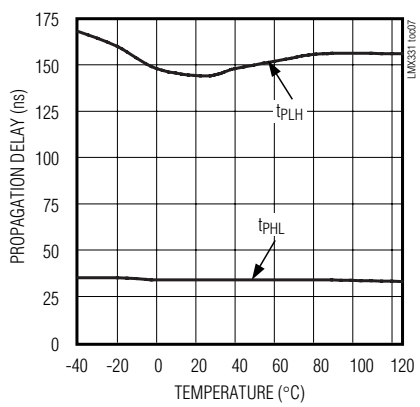
OUTPUT LOW VOLTAGE vs. TEMPERATURE



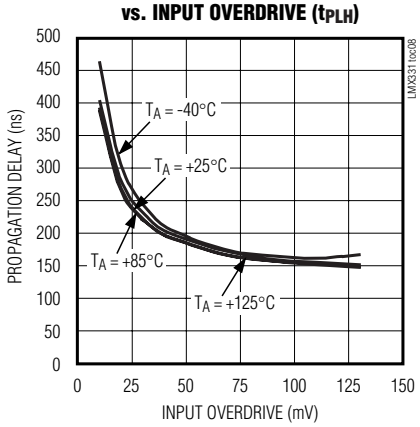
PROPAGATION DELAY vs. CAPACITIVE LOAD



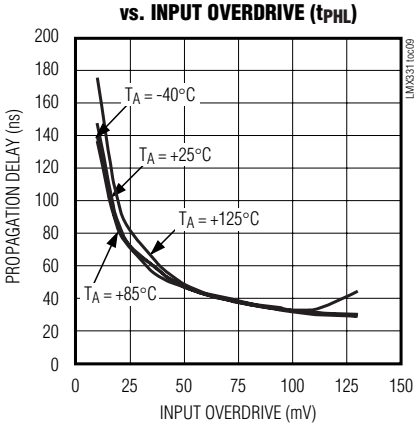
PROPAGATION DELAY vs. TEMPERATURE



PROPAGATION DELAY vs. INPUT OVERDRIVE (t_{PLH})



PROPAGATION DELAY vs. INPUT OVERDRIVE (t_{PHL})

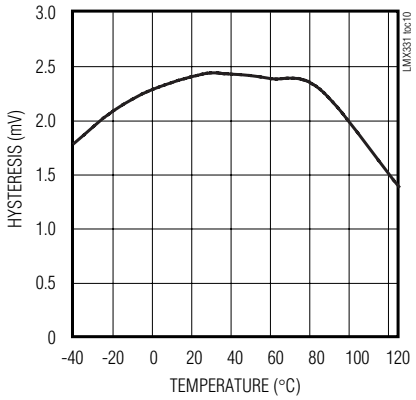


General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

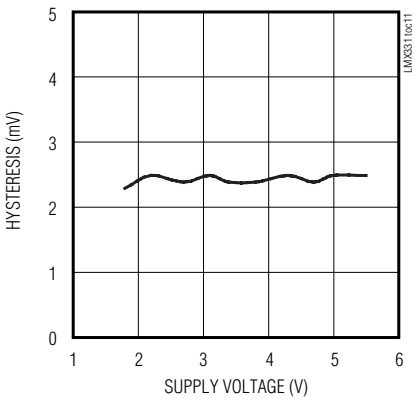
Typical Operating Characteristics (continued)

(V_{DD} = 5V, V_{SS} = 0, V_{CM} = 0, R_L = 5.1kΩ, C_L = 10pF, overdrive = 100mV, T_A = +25°C, unless otherwise noted.)

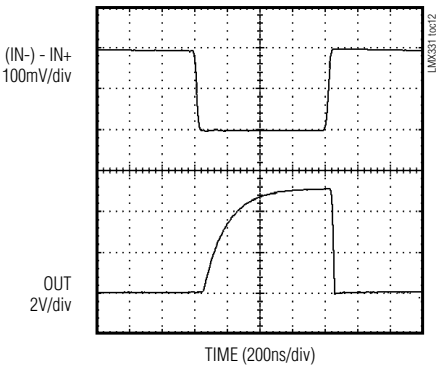
LMX331H/LMX393H/LMX339H HYSTERESIS vs. TEMPERATURE



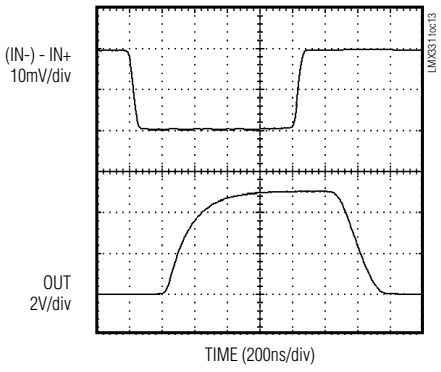
LMX331H/LMX393H/LMX339H HYSTERESIS vs. SUPPLY VOLTAGE



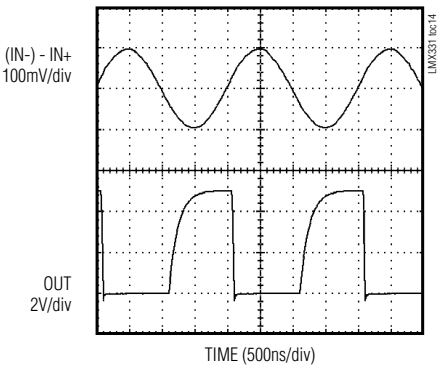
PROPAGATION DELAY 100mV OVERDRIVE



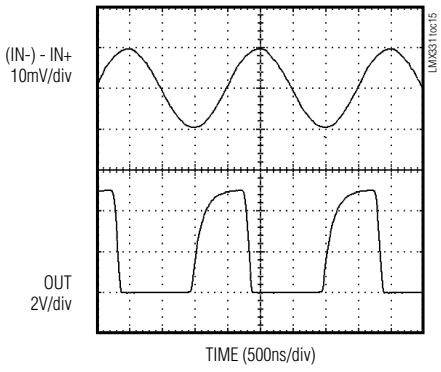
PROPAGATION DELAY 10mV OVERDRIVE



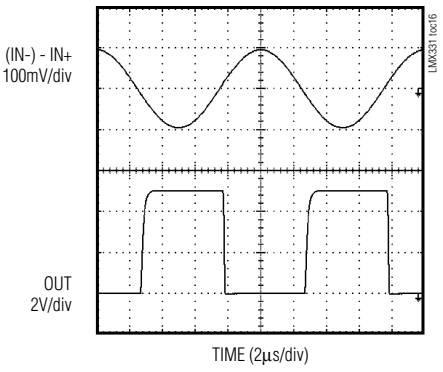
500kHz RESPONSE 100mV OVERDRIVE



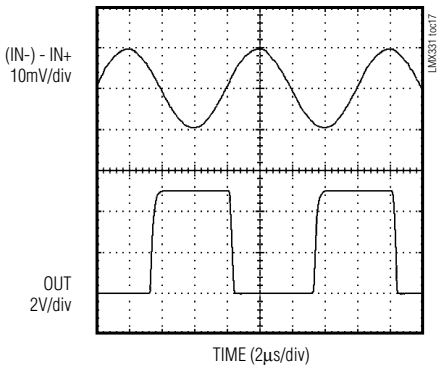
500kHz RESPONSE 10mV OVERDRIVE



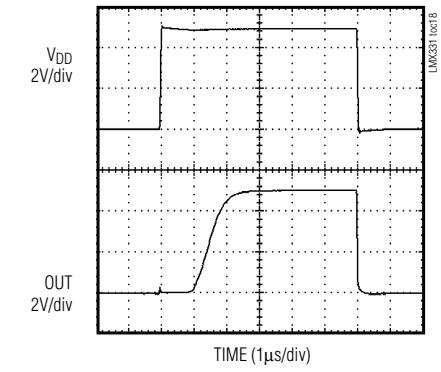
100kHz RESPONSE 100mV OVERDRIVE



100kHz RESPONSE 10mV OVERDRIVE



POWER-UP RESPONSE



Pin Description

PIN			NAME	FUNCTION
LMX331	LMX393	LMX339		
1	—	—	IN+	Noninverting Input
2	4	12	V_{SS}	Negative Supply (Connect to GND)
3	—	—	IN-	Inverting Input
4	—	—	OUT	Comparator Output (Open-Drain)
5	8	3	V_{DD}	Positive Supply
—	1	2	OUTA	Comparator A Output (Open-Drain)
—	7	1	OUTB	Comparator B Output (Open-Drain)
—	2	4	INA-	Comparator A Inverting Input
—	3	5	INA+	Comparator A Noninverting Input
—	5	7	INB+	Comparator B Noninverting Input
—	6	6	INB-	Comparator B Inverting Input
—	—	8	INC-	Comparator C Inverting Input
—	—	9	INC+	Comparator C Noninverting Input
—	—	10	IND-	Comparator D Inverting Input
—	—	11	IND+	Comparator D Noninverting Input
—	—	13	OUTD	Comparator D Output (Open-Drain)
—	—	14	OUTC	Comparator C Output (Open-Drain)

Detailed Description

The LMX331/LMX393/LMX339 are single/dual/quad, low-cost, general-purpose comparators. They have a single-supply operating voltage of 1.8V to 5V. The common-mode input range extends from -0.1V below the negative supply to within 0.7V of the positive supply. They require approximately 60µA per comparator with a 5V supply and 40µA with a 2.7V supply.

The LMX331H/LMX393H/LMX339H have 2mV of hysteresis for noise immunity. This significantly reduces the chance of output oscillations even with slow-moving input signals. The LMX331/LMX393/LMX339 and LMX331H/LMX393H/LMX339H are ideal for automotive applications because they operate from -40°C to +125°C (see *Typical Operating Characteristics*).

Applications Information

Hysteresis

Many comparators oscillate in the linear region of operation because of noise or undesired parasitic feedback. This tends to occur when the voltage on one input is equal or very close to the voltage on the other input. The LMX331H/LMX393H/LMX339H have internal hysteresis to counter parasitic effects and noise.

The hysteresis in a comparator creates two trip points: one for the rising input voltage and one for the falling input voltage (Figure 1). The difference between the trip points is the hysteresis. When the comparator's input voltages are equal, the hysteresis effectively causes one comparator input to move quickly past the other, thus taking the input out of the region where oscillation occurs. This provides clean output transitions for noisy, slow-moving input signals.

Additional hysteresis can be generated with two resistors, using positive feedback (Figure 2). Use the following procedure to calculate resistor values:

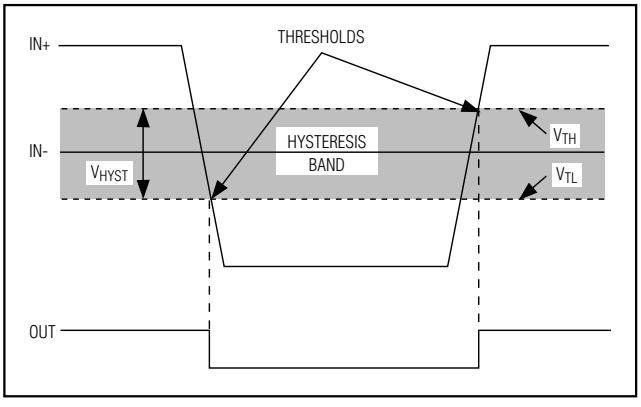


Figure 1. Threshold Hysteresis Band (Not to Scale)

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

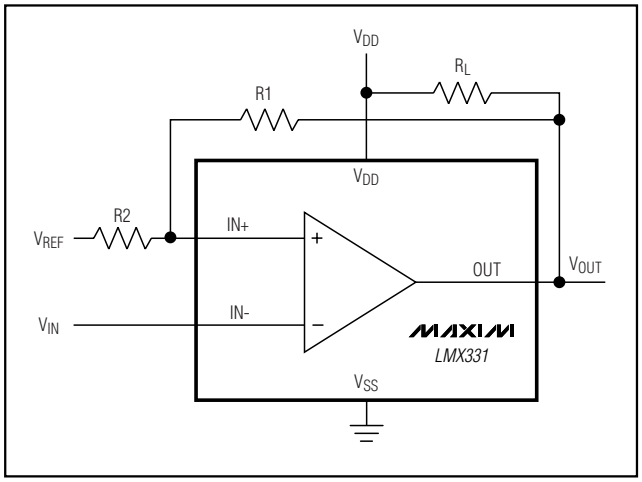


Figure 2. Adding Hysteresis with External Resistors

1) Find output voltage when output is high:

$$V_{OUT(HIGH)} = V_{DD} - I_{LOAD} \times R_L$$

2) Find the trip points of the comparator using these formulas:

$$V_{TH} = V_{REF} + ((V_{OUT(HIGH)} - V_{REF})R2) / (R1 + R2)$$

$$V_{TL} = V_{REF}(1 - (R2 / (R1 + R2)))$$

where V_{TH} is the threshold voltage at which the comparator switches its output from high to low as V_{IN} rises above the trip point, and V_{TL} is the threshold voltage at which the comparator switches its output from low to high as V_{IN} drops below the trip point.

3) The hysteresis band will be:

$$V_{HYST} = V_{TH} - V_{TL} = V_{DD}(R2 / (R1 + R2))$$

In this example, let V_{DD} = 5V, V_{REF} = 2.5V, I_{LOAD} = 50nA, R_L = 5.1kΩ:

$$V_{OUT(HIGH)} = 5.0V - (50 \times 10^{-9} \times 5.1 \times 10^{3}\Omega) \approx 5.0V$$

$$V_{TH} = 2.5V + 2.5V(R2 / (R1 + R2))$$

$$V_{TL} = 2.5V(1 - (R2 / (R1 + R2)))$$

Select R2. In this example, we will choose 1kΩ.

Select V_{HYST}. In this example, we will choose 50mV.

Solve for R1:

$$V_{HYST} = V_{OUT(HIGH)}(R2 / (R1 + R2))\ V$$

$$0.050V = 5(1000 / (R1 + 1000))\ V$$

where R1 ≈ 100kΩ, V_{TH} = 2.525V, and V_{TL} = 2.475V.

Choose R1 and R2 to be large enough as not to exceed the amount of current the reference can supply.

The source current required is V_{REF} / (R1 + R2).

The sink current is ($V_{OUT(HIGH)}$ - V_{REF}) × (R1 + R2).

Choose R_L to be large enough to avoid drawing excess current, yet small enough to supply the necessary current to drive the load. R_L should be between 1kΩ and 10kΩ.

Board Layout and Bypassing

Use 0.1µF bypass capacitors from V_{DD} to V_{SS}. To maximize performance, minimize stray inductance by putting this capacitor close to the V_{DD} pin and reducing trace lengths. For slow-moving input signals (rise time > 1ms), use a 1nF capacitor between IN+ and IN- to reduce high-frequency noise.

Chip Information

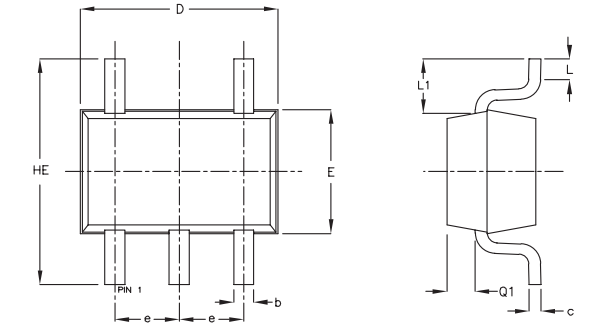
LMX331/LMX331H TRANSISTOR COUNT: 112

LMX393/LMX393H TRANSISTOR COUNT: 211

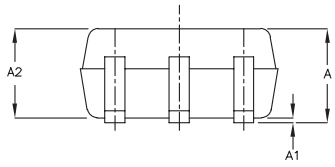
LMX339/LMX339H TRANSISTOR COUNT: 411

Package Information

SC70, 5L.EPS



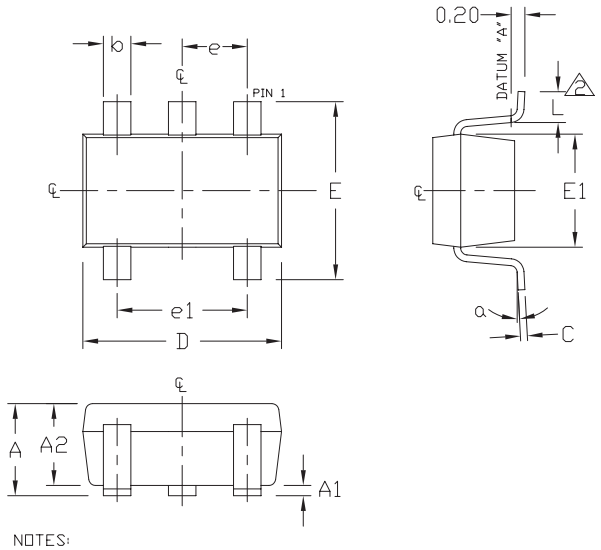
SYMBOL	MIN	MAX
e	0.65 BSC	
D	1.80	2.20
b	0.15	0.30
E	1.15	1.35
HE	1.80	2.40
Q1	0.10	0.40
A2	0.80	1.00
A1	0.00	0.10
A	0.80	1.10
c	0.10	0.18
L	0.10	0.30
L1	0.425 TYP.	



NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS
2. DIMENSIONS ARE INCLUSIVE OF PLATING
3. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH & METAL BURR
4. ALL SPECIFICATIONS COMPLY TO EIAJ SC70
5. COPLANARITY 4 MILS. MAX.

MAXIM
PROPRIETARY INFORMATION
TITLE: PACKAGE OUTLINE, SC70, 5L
APPROVAL | DOCUMENT CONTROL NO. 21-0076 | REV B | 1/1

SOT5L.EPS



SYMBOL	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.35	0.50
C	0.08	0.20
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.75
L	0.35	0.55
e	0.95 REF	
e1	1.90 REF	
a	0°	10°

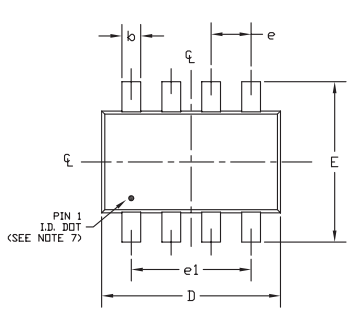
NOTES:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM A & LEAD SURFACE.
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.
5. MEETS JEDEC MO178.

MAXIM
PROPRIETARY INFORMATION
TITLE: PACKAGE OUTLINE, SOT-23, 5L
APPROVAL | DOCUMENT CONTROL NO. 21-0057 | REV C | 1/1

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

Package Information (continued)

SOT23, 8L.EPS



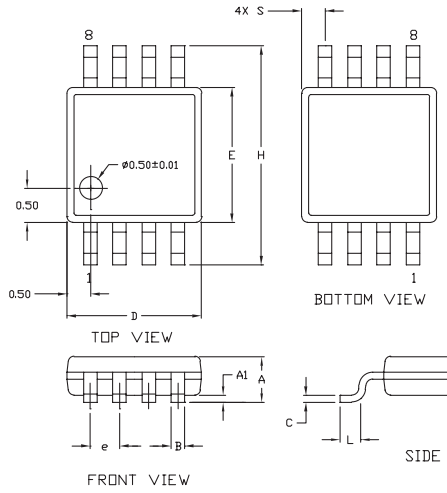
SYMBOL	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.28	0.45
C	0.09	0.20
D	2.80	3.00
E	2.60	3.00
E1	1.50	1.75
L	0.10	0.60
e	0.65 ref	
e1	1.95 ref	
α	0°	10°

NOTE:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. FOOT LENGTH MEASURED REFERENCE TO FLAT FOOT SURFACE PARALLEL TO DATUM "A".
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.
5. EIAJ REF. NUMBER" SC-74 (6 LEAD VERSION)
6. COPLANARITY 4 MILS. MAX.
7. PIN 1 I.D. DOT IS 0.3 MM ø MIN. LOCATED ABOVE PIN 1.
8. MEETS JEDEC MO178.

MAXIM
PROPRIETARY INFORMATION

TITLE: PACKAGE OUTLINE, SOT-23, 8L			
APPROVAL	DOCUMENT CONTROL NO. 21-0078	REV C	1/1

8LUMAXD.EPS



	INCHES		MILLIMETERS		JEDEC INCHES		JEDEC MILLIMETERS	
	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
A	0.037	0.043	0.94	1.10	---	0.043	---	1.10
A1	0.002	0.006	0.05	0.15	0.002	0.006	0.05	0.15
B	0.010	0.014	0.25	0.36	0.010	0.016	0.25	0.40
C	0.005	0.007	0.13	0.18	0.005	0.009	0.13	0.23
D	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
e	0.0256 BSC		0.65 BSC		0.0256 BSC		0.64 BSC	
E	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
H	0.188	0.198	4.78	5.03	0.193 BSC		4.9 BSC	
L	0.016	0.026	0.41	0.66	0.016	0.027	0.40	0.70
α	0°	6°	0°	6°	0°	6°	0°	6°
S	0.0207 BSC		0.5250 BSC					

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187.

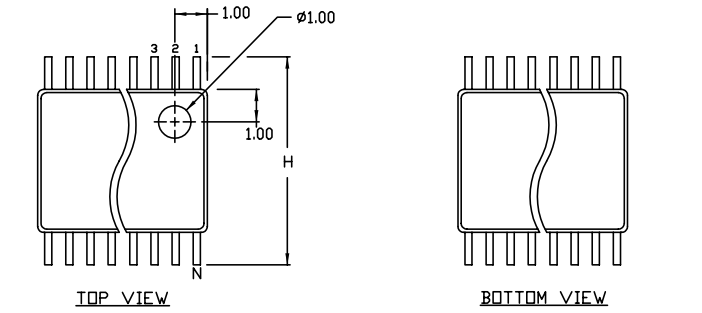
MAXIM
PROPRIETARY INFORMATION

TITLE: PACKAGE OUTLINE, 8L uMAX			
APPROVAL	DOCUMENT CONTROL NO. 21-0036	REV I	1/1

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

Package Information (continued)

TSSOP,NO PADS.EPS



SYMBOL	COMMON DIMENSIONS			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	———	1.10		.043
A_1	0.05	0.15	.002	.006
A_2	0.85	0.95	.033	.037
b	0.19	0.30	.007	.012
b1	0.19	0.25	.007	.010
c	0.090	0.20	.0035	.008
c1	0.090	0.135	.0035	.0053
D	SEE VARIATIONS		SEE VARIATIONS	
E	4.30	4.50	.169	.177
e	0.65 BSC		.026 BSC	
H	6.25	6.50	.246	.256
L	0.50	0.70	.020	.028
N	SEE VARIATIONS		SEE VARIATIONS	
α	0°	8°	0°	8°

JEDEC			VARIATIONS			
MO-153	N		MILLIMETERS		INCHES	
			MIN.	MAX.	MIN.	MAX.
AB-1	14	D	4.90	5.10	.193	.201
AB	16	D	4.90	5.10	.193	.201
AC	20	D	6.40	6.60	.252	.260
AD	24	D	7.70	7.90	.303	.311
AE	28	D	9.60	9.80	.378	.386

NOTES:
1. DIMENSIONS D AND E DO NOT INCLUDE FLASH
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15mm PER SIDE
3. CONTROLLING DIMENSION: MILLIMETER
4. MEETS JEDEC OUTLINE MO-153. SEE JEDEC VARIATIONS TABLE.
5. "N" REFERS TO NUMBER OF LEADS
6. THE LEAD TIPS MUST LIE WITHIN A SPECIFIED ZONE. THIS TOLERANCE ZONE IS DEFINED BY TWO PARALLEL PLANES. ONE PLANE IS THE SEATING PLANE, DATUM [-C-]; THE OTHER PLANE IS AT THE SPECIFIED DISTANCE FROM [-C-] IN THE DIRECTION INDICATED.

MAXIM

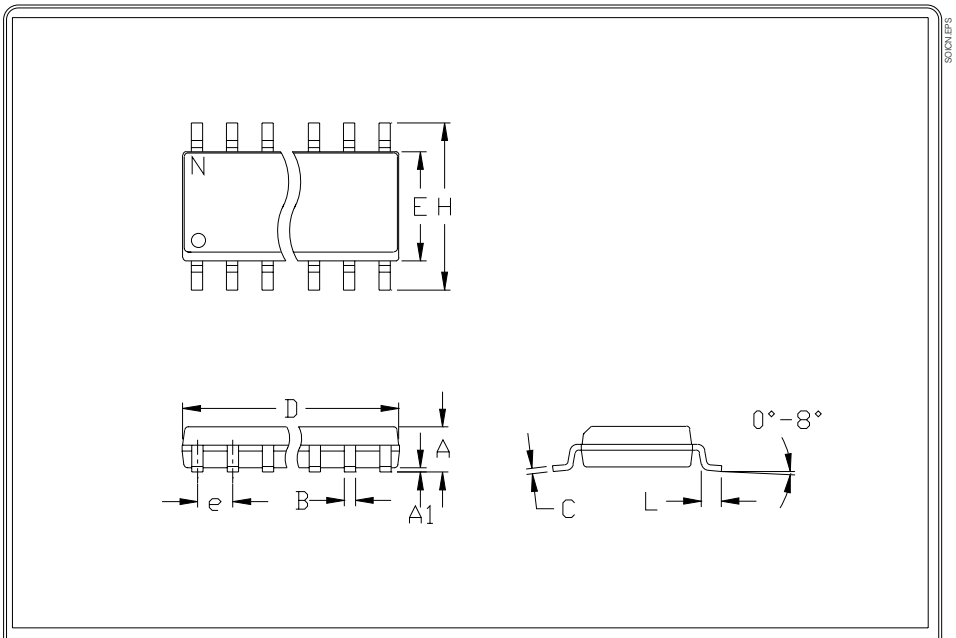
PROPRIETARY INFORMATION

TITLE: PACKAGE OUTLINE, TSSOP, 4.40 MM BODY

APPROVAL	DOCUMENT CONTROL NO.	REV	
	21-0066	E	1/1

General-Purpose, Low-Voltage, Single/Dual/Quad, Tiny-Pack Comparators

Package Information (continued)



	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
e	0.050		1.27	
E	0.150	0.157	3.80	4.00
H	0.228	0.244	5.80	6.20
h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27

	INCHES		MILLIMETERS			
	MIN	MAX	MIN	MAX	N	MS012
D	0.189	0.197	4.80	5.00	8	A
D	0.337	0.344	8.55	8.75	14	B
D	0.386	0.394	9.80	10.00	16	C

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006")
3. LEADS TO BE COPLANAR WITHIN .102mm (.004")
4. CONTROLLING DIMENSION: MILLIMETER
5. MEETS JEDEC MS012-XX AS SHOWN IN ABOVE TABLE
6. N = NUMBER OF PINS

MAXIM 120 SAN GABRIEL DR SUNNYVALE CA 94086 FAX (408) 737 7194
PROPRIETARY INFORMATION

PACKAGE FAMILY OUTLINE: SOIC .150"
TITLE

1/1

21-0041 A
DOCUMENT CONTROL NUMBER REV